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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/098,550	03/18/2002	Lin-Hung Chen	MR3003-13	4945
4586	7590	01/05/2005	EXAMINER	
ROSENBERG, KLEIN & LEE 3458 ELLICOTT CENTER DRIVE-SUITE 101 ELLICOTT CITY, MD 21043			HUYNH, KIM T	
			ART UNIT	PAPER NUMBER
			2112	

DATE MAILED: 01/05/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Applicant(s)

10/098,550

Applicant(s)

CHEN, LIN-HUNG

Examiner

Kim T. Huynh

Art Unit

2112

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 12 October 2004.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1,3,5-7,9,11-14 and 16-18 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1,3,5-7,9,11-14 and 16-18 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 18 March 2002 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1, 3, 5, 6-7, 9, 11-14, 16-18 are rejected under 35 U.S.C. 102(e) as being anticipated by Gafken et al. (US Patent 6,157,970)

As per claim 1, Gafken discloses a circuit system for data transmission between LPC devices, comprising;

- A first LPC bus, (fig.1, 124 ie LPC bus) connected to a first LPC device(fig.1, 104, ie peripheral devices), (col.2, lines 59-67);
- A second LPC bus, (fig.1, 124, ie LPC bus) connected to a second LPC device(fig.1, 106, ie peripheral device), (col.2, lines 59-67);
- and
- An LPC host controller(fig.1, 102, ie host), including an address register (col.7, lines 22-35, ie LAD[3:0])for storing a target address, said LPC host controller(col.6, lines 13-67) being operable to initiate a first bus access cycle on said first LPC bus and a second bus access cycle on said second LPC bus, said LPC host controller

being further operable to initiate said second access cycle upon said target address matching(col.9, lines 25-33) an address on said second LPC bus, said LPC host controller being configured to terminate said first bus access cycle only after said second bus access cycle is terminated.(col.5, lines 5-14), (col.8, lines 41-52)

As per claim 3, Gafken discloses wherein said first LPC device is a master LPC device and said second LPC device is a slave LPC device.(col.5,lines 15-20, wherein transactions is performing between any two devices, master(transmitter source) and slave(receiver target)

As per claim 5, Gafken discloses wherein said first LPC bus and second LPC bus are connected to a plurality of LPC devices (col.4, lines 3-6, fig.1, 104,106,108 ie peripheral devices couple on LPC bus 124), respectively.

As per claim 6, Gafken discloses a method for data transmission between LPC devices, comprising the steps of:

- Providing an LPC host controller with an address register for storing a target address;(col.7, lines 22-35, ie LAD[3:0])
- Initiating a first bus access cycle on a first LPC bus by said LPC host controller; (col.8,lines 41-65)
- Transmitting over said first LPC bus by a first LPC device coupled thereto a request to said LPC host controller for a transaction with a second LPC device coupled to a second LPC bus;(col.11,lines 46-52), (col.6,lines 13-67)

- Storing an address of a data location on said second LPC device in said address register as said target address;(col.7,lines 22-35)
- Inserting a plurality of wait states in said first bus access cycle after said request is received by said LPC host controller; and (col.10,lines 37-67)
- Initiating a second bus access cycle on a said second LPC bus by said LPC host controller; and (col.7,lines 59-67)
- Accessing by said LPC host controller said data location over said second LPC bus.(col.7,lines 22-46)

As per claim 7, Gafken discloses the method including the steps of:

- Setting said transaction to be a data read from said second LPC device; (col.8,lines 41-65)
- Transferring said data from said second LPC device to said LPC host controller over said second LPC bus;(col.7,lines 59-67)
- Terminating said bus access cycle on said second LPC bus;(col.5,lines 5-14)
- Terminating said wait state inserting step after said bus access cycle on said second LPC bus is terminated;(col.10,lines 37-67)
- Transferring said data from said LPC host controller to said first LPC device over said first LPC bus; and (col.8,lines 41-52)
- Terminating said bus access cycle on said first LPC bus after said LPC host controller transfers said data to said first LPC device.(col.10,lines 37-67)

As per claim 9, Gafken discloses the method further including the steps of:

- Setting said transaction to be a data write to said second LPC device;(col.8, lines 41-65)
- Transferring said data from said first LPC device to said LPC host controller over said first LPC bus;(col.8,lines 41-52)
- Transferring said data from said LPC host controller to said first LPC device over said second LPC bus; and (col.11, lines 46-52)
- Terminating said bus access cycle on said second LPC bus; (col.5, lines 5-14)
- Terminating said wait state inserting step after said bus access cycle on said second LPC bus is terminated; (col.10, lines 37-67)
- Terminating said bus access cycle on said first LPC bus after said LPC host controller transfers said data to said second LPC device.(col.10, lines 37-67)

As per claim 11, Gafken discloses a circuit system for data transmission between LPC devices, comprising:

- An LPC bus;(fig.1, 124)
- A master LPC device connected to said LPC bus, said master LPC device including an address register for storing a target address;(col.7, lines 22-35)
- At least one slave LPC device connected to said LPC bus; and(col.4, lines 3-13)

- An LPC host controller(fig.1, 102, ie host), including an address register for storing said target address, said LPC host controller being operable to initiate a data transfer cycle on said LPC bus, said data transfer cycle including both a first bus access cycle for LPC bus access by said master LPC device and by a second bus access cycle for LPC bus access by one of said at least one slave LPC device. (col.8, lines 41-52, wherein transmitter(master) and receiver(slave), transaction can be performed between any two devices ie 104,106, 108 or host 102)

As per claim 12, Gafken discloses wherein each of said at least one slave LPC device includes an address register for storing said target address.(col.7, lines 22-35)

As per claim 13, Gafken discloses a method for data transmission between LPC devices, comprising the steps of:

- Providing an LPC master device with an address register for storing a target address;(col.7, lines 22-35)
- Initiating a first bus access cycle on an LPC bus by said LPC host controller;(col.8, lines 41-65)
- Transmitting a request for a transaction over said LPC bus from said master LPC device to said LPC host controller, said transaction specifying a transfer of first data between said master LPC device and a slave LPC device; (col.6, lines 13-67)

- Transferring second data between said LPC host controller and master LPC device during said first bus access cycle;(col.8, lines 41-52)
- Storing an address of a data location on said slave LPC device in said address register of said master LPC device as said target address;(col.7, lines 22-35)
- Initiating a second bus access cycle on said LPC bus by said LPC host controller; (col.8, lines 41-65)
- Transferring said first data between said LPC host controller said slave LPC device during said second bus access cycle.(col.7, lines 59-67)

As per claim 14, Gafken discloses the method further including the steps of:

- Setting said transaction to be a data read from said slave LPC device; and (col.8, lines 11-65)
- Setting said second data to an arbitrary data value.(col.8, lines 11-65)

As per claim 16, Gafken discloses the method further comprising the steps of:

- Providing said LPC host controller with an address register for storing said target address; and (col.7, lines 22-35)
- Storing said address of said data location in said address register of said LPC host controller after said LPC host controller has

received said transaction request from said master LPC device.

(col.7, lines 22-46)

As per claim 17, Gafken discloses the method further comprising the steps of:

- Transferring said first data from said slave LPC device to said LPC host controller; and (col.6, lines 13-67), (col.8, lines 41-65)
- Monitoring said LPC bus for data transferred from said slave LPC device; and (col.10, lines 56-67)
- Accepting at said master LPC device said data transferred from said at least one slave LPC device as said first data if a source address thereof is equivalent to said target address stored in said address register of said master LPC device. (col.8, lines 41-65), (col.9, lines 25-34)

As per claim 18, Gafken discloses the method further including the steps of:

- Setting said transaction to be a data write to said slave LPC device; and (col.6, lines 35-67)
- Setting said second data to equal said first data. (col.6, lines 35-67), (col.8, lines 41-65)

Response to Amendment

3. Applicant's amendment filed on 10/12/04 have been fully considered but are moot in view of the new ground(s) of rejection.

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Conclusion

4. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

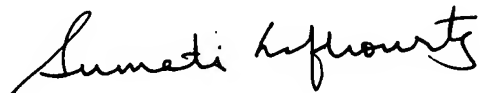
A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

5. *Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kim Huynh whose telephone number is (571)272-3635 or via e-mail addressed to [kim.huynh3@uspto.gov]. The examiner can normally be reached on M-F 9:00AM-6:00PM. The fax phone numbers for the organization where this application or proceeding is assigned are (703)872-9306 for regular communications and After Final communications.*

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (571)272-2100.

Kim Huynh

Dec. 26, 2004


SUMATI LEFKOWITZ
PRIMARY EXAMINER